

Thin film deposition for next generation DRAM structures





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NTNU



40000 students 50 faculties Nobel prize winner 2014

10/16/2017

NTNU



NTNU Team

Our group:

3 Faculty
10 PhD students
15 Master Students/ year
Addlab, Fatigue and Nanolab
~ Supported by workshop and admin staff

Research Directions:

Material Design Material Property Prediction Surface and Interface Science Biocompatible Implants Light weight components Protective Coatings







Atomic Layer Deposition- Motivation

Moore's law

- One of humanity's greatest achievements
- Over 40 years of exponential improvement
- What is the limit?

16-Core SPARC T3 Six-Core Core ii 2,600,000,000 Six-Core Xeon 7400 Core Xeon Westmere EX Dual-Core Itanium 2 uad-core z196 uad-Core Itanium Tukwila AMD K10 1,000,000,000 8-Core Xeon Nehalem-EX Six-Core Opteron 2400 POWER6 Itanium 2 with 9MB cache ore 2 Duo Itanium 2 AMD K8 100.000.000 Barton Atom Pentium AMB K7 curve shows transistor AMD K6 count doubling every Transistor count 10,000,000 Pentium III two vears Pentium II AMD K5 ●Pentium 80486 1.000.000-80386 80286 100,000-68000 80186 80860 98088 10,000-6809 80.80 80.08 MOS 6502 2,300-40.04 • RCA 1802 2011 2000 1980 1990 1971 Date of introduction

Microprocessor Transistor Counts 1971-2011 & Moore's Law

Atomic Layer Deposition- Motivation





How to Increase the Capacitance? $U = \frac{1}{2}CV^{2} \qquad C = e_{0}e_{r}\frac{A}{d}$

Atomic Layer Deposition- Motivation



Smallest die size:

0.683 mm × 0.683 mm at the 90 nm 151.527 dies

Average die size

2.130 mm × 2.130 mm at the 65 nm 1434 dies

Biggest die size: 20.253 mm × 20.253 mm at the 65 nm 127 dies

Core i3-2310E Transistors: 624 million Die size: Average (149 mm²)

Atomic Layer Deposition- The principle



Oxidant pulse

Atomic Layer Deposition- Scale Up



Atomic Layer Deposition- Scale Up



<u>ASM A400C</u>

- Batch size of 125 8-inch wafers
- Dual tube for material stacks
- Process line integration
- Different wafer sizes processable
- Major applications for silicon nitride, amorphous silicon, doped polysilicon
- Wide range of metals and dielectrics processable





Atomic Layer Deposition- Scale Up





<u>Solaytec</u>

- Wafer moves back and forth performing 4 cycles at a time
- 8 depositions/s with one head
- 5000 wafers/h
- Atmospheric pressure, no pump required
- Deposition rate 1nm/s per module
- Only TMA and H₂O so far



http://www.solaytec.com/images/stories/flexicontent/l_solaytec-2919-v1.jpg

Atomic Layer Deposition- The materials

Depositing elements all across the periodic table



Intel's 45-nm high-k transistor



Mistry, K. et al, *Electron Devices Meeting*, 2007. *IEDM* 2007. *IEEE International*, pp.247-250 (2007)

Nanoprobe for protein detection in cells



Shambat et al.; Nano Lett. 2013, 13, 4999-5005.

Thin film solid oxide fuel cell



Chao, C. C., Hsu, C. M., Cui, Y., Prinz, F. P., ACS Nano, 5, 5692-5696 (2011).

How to Increase the Capacitance?

$$U = \frac{1}{2}CV^2 \qquad C = \theta_0 \theta_r \left(\frac{A}{d}\right)$$

 \frown



High-k materials







Crystallites buried in amorphous matrix of 7 nm thick $BaTiO_3$



Knoops et al. J. El. Chem. Soc. 2010



GPC ~ 0.45 Å/cycle



Self-limiting mode of growth in both half cycles of reaction



| La | Се | Pr | Nd | Pm | Sm | Eu | Gd | Tb | Dy | Но | Er | Tm | Yb | Lu |
|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|
| Ac | Th | Ра | U | Np | Pu | Am | Cm | Bk | Cf | Es | Fm | Md | No | Lr |



lanthanides † 4f (rare earth metals)

actinides ± 5f





ALD characterization in the SSRL

What is the chemical and structural nature for thin film performance modifications?



Provides

Chemical Identity Oxidation State Coordination Local geometric Structure Local Density of unoccupied States

Allows studying

Interfaces Structural distortions Dopant atoms Nucleation process

ALD characterization in the SSRL



Beer's law: Absorbance= $\log(I_0/I_t)$ or $\log(I_0/I_f)$

X-ray absorption near edge structure (XANES)



XANES of ALD BTO

Mixing of BaO and TiO₂ to form BaTiO₃



XANES of ALD BTO

Changing Ba/Ti composition

Energy (eV)

Leakage current and band gap

- Aspect ratio of the trench ~ 1:3.9
- Step coverage (d_{bottom}/d_{top}) ~ 90%
- Uniform composition distribution.

Summary

- ALD for reaching the ultimate limit in downascaling
- ALD high-k Barium Titanate for next generation DRAM structures
- Novel chemistry for self limiting growth of BTO
- Explanation of dielectric properties with electronic structure revealed by synchroton based X-ray absorption

Shinjita

Yongmin

Ioannis

We create chemistry

Anup

Ongoing activity with TU Wien

Additive Manufacturing Technical Commitee

ESIS Technical committee 15 (Chief) ESFRI HORIZON 2020 PROPOSAL (180 ME, 13 PARTNERS)